

## REMARKS

Claims 1 and 2 were rejected under 35 U.S.C. §102 as being anticipated by LeQuere (Pub. No. US-2003/0014452 A1) in Fig. 5. The Examiner asserts that Reference A (LeQuere), Fig. 5 shows a true random number generator comprising a first oscillator (Clock 8), a second oscillator (Clock 7), a frequency multiplication means (Divider 11) and a processor means (elements 10, 12 and 13).

The Examiner's rejection is respectfully traversed. The term Divider as used in LeQuere's Fig. 5 indicates a counter for reducing the frequency from its input. This term is always used to mean dividing an input frequency by some integer factor to produce a lower frequency output. A frequency multiplier of any type is always used to indicate a means for increasing the frequency from its input. Applicant submits that a divider can never be used as a frequency multiplication means and therefore the depiction of a divider in Fig. 5 does not anticipate the function or use of a frequency multiplier.

The frequency multiplier of applicant's invention provides new results of additional entropy that is extracted by the processor means. The frequency multiplier also makes it easier to extract the entropy from the first and second oscillators, allowing for more complete entropy gathering and hence, a higher true random number generation rate.

Applicant's Claims 1 and 2 include a frequency multiplier, which is not illustrated in or anticipated by LeQuere's Fig. 5. Therefore applicant submits that Claims 1 and 2 are allowable over the cited reference and solicits reconsideration and allowance.

Claims 3 and 4 were objected to as being dependent on a rejected base claim. The Examiner's objection is respectfully traversed. Claims 3 and 4 are dependent on Claim 1. Applicant submits that Claim 1 is allowable in the light of the reasons stated above with respect to Claims 1 and 2. Therefore, applicant further submits that dependent Claims 3 and 4 are also allowable and applicant solicits reconsideration and allowance of Claims 3 and 4.

**Applicant thanks the Examiner for allowance of Claims 5-15.**

The claims have been amended to remove certain terms, such as "means" and "steps", which may unnecessarily narrow the interpretation of these claims.

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## CONCLUSION

For the reasons given above, applicant respectfully submits the Claims 1 through 4 define over the prior art under Section 102 because the combination of a first and a second oscillator, a frequency multiplier and a processor is not illustrated or anticipated by LeQuere. Furthermore, the claimed distinctions are of patentable merit under Section 103 because of the new results of the additional entropy source of the frequency multiplier, and the more efficient entropy extraction and the higher true random generation rate afforded. Applicant respectfully submits further that the amended claims without the terms, "means" and "steps", better specify the patentable matter contained in the application. Accordingly, applicant submits that this application is in condition for allowance, and applicant respectfully solicits reconsideration and allowance. If this response is in any way unsatisfactory or if any minor amendments can place this application in condition for allowance, it is respectfully requested that the Examiner telephone the undersigned so as to facilitate the rapid progress of this application through the USPTO.

Applicant has reviewed the additional prior-art references. These do not describe or suggest applicant's invention.

Respectfully Submitted,



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